

REMARKS

The above preliminary amendment is made to remove multiple dependencies from claims 4, 5, 6, 7, 8, 9, 10 and 17.

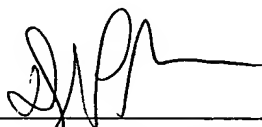
Applicants respectfully request that the preliminary amendment described herein be entered into the record prior to calculation of the filing fee and prior to examination and consideration of the above-identified application.

If a telephone conference would be helpful in resolving any issues concerning this communication, please contact Applicants' primary attorney-of record, Douglas P. Mueller (Reg. No. 30,300), at (612) 371.5237.

Respectfully submitted,

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By 

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5. (Amended) The component built-in module according to [any one of claims 1 to 3] claim 1, comprising a core layer formed of an electric insulating material; an electric insulating layer comprising an electric insulating material formed of a mixture including an inorganic filler and a thermosetting resin, which is formed on at least one surface of the core layer; and a plurality of wiring patterns formed of a copper foil; wherein the core layer has a plurality of wiring patterns formed of a copper foil and a plurality of inner vias formed of a conductive resin, and the wiring patterns are connected electrically to each other by the inner vias.

6. (Amended) The component built-in module according to [any one of claims 1 to 3] claim 1, comprising a core layer formed of an electric insulating material; an electric insulating layer comprising an insulating material formed of a thermosetting resin, which is formed on at least one surface of the core layer; and a plurality of wiring patterns formed by copper-plating; wherein the core layer has a plurality of wiring patterns formed of a copper foil and a plurality of inner vias formed of a conductive resin, and the wiring patterns formed by the copper-plating are connected electrically to each other by the inner vias.

7. (Amended) The component built-in module according to [any one of claims 1 to 3] claim 1, comprising a core layer formed of an electric insulating material; an electric insulating layer

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17. (Amended) The method for producing the component built-in module according to claim 14 [or 16], wherein the film-shaped component is at least one selected from the group consisting of a resistor, a capacitor and an inductor, which is formed of a thin film or a mixture comprising an inorganic filler and a thermosetting resin; and the film-shaped component is formed by one method selected from the group consisting of vapor deposition method, MO-CVD method or a thick film printing method.

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